

Fig 1
(PRIOR ART)

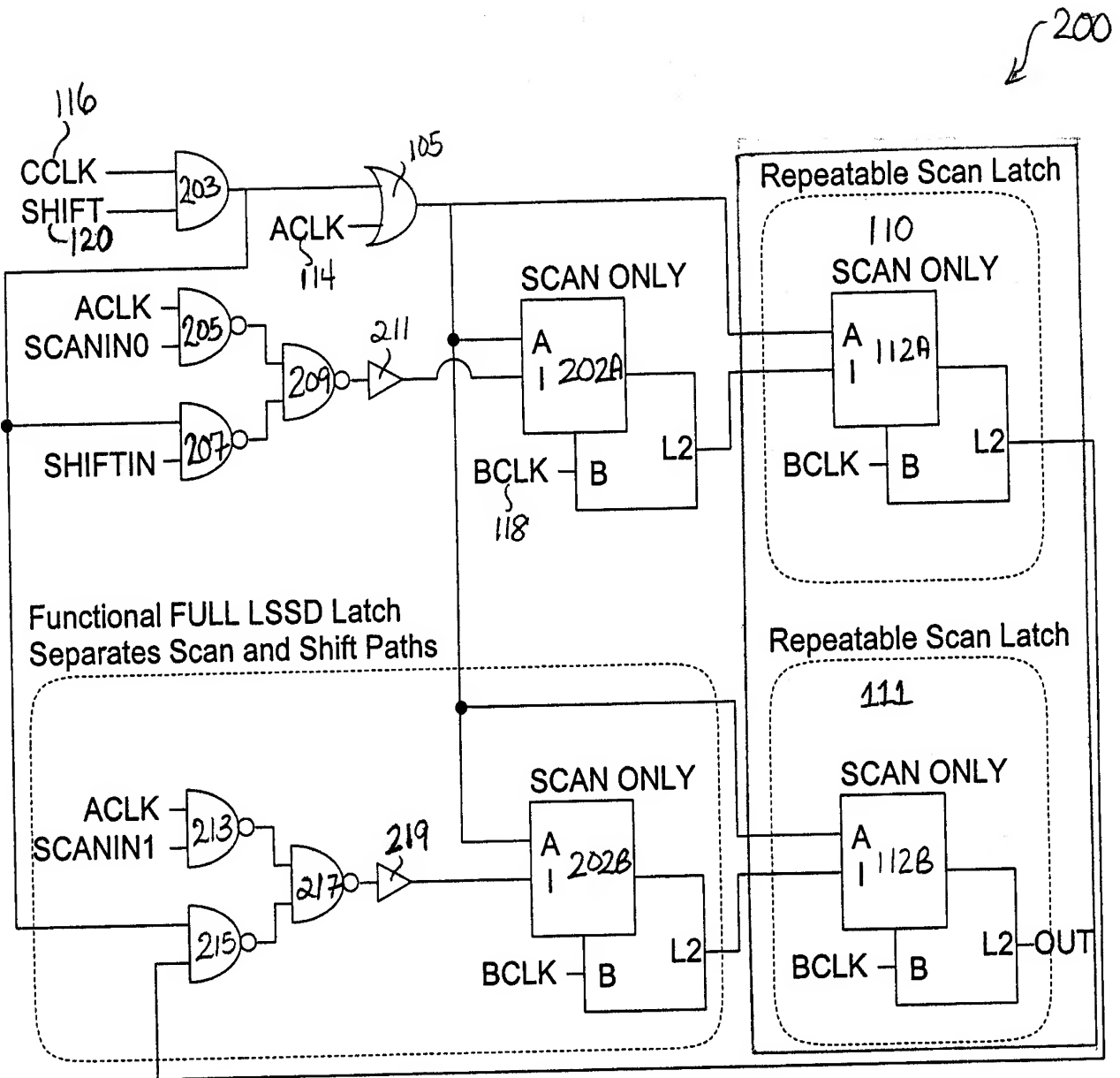
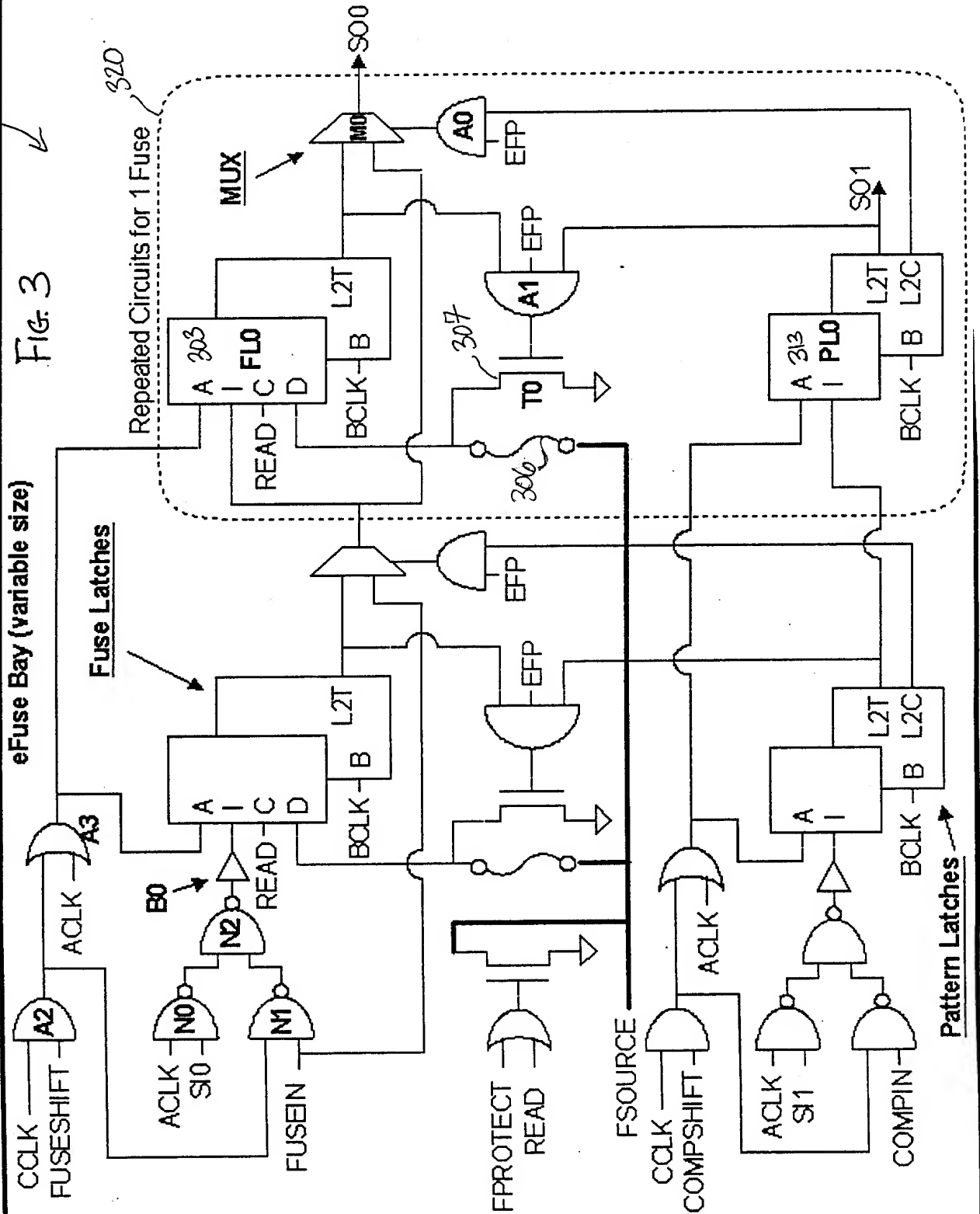


FIGURE 2

Fig. 3

eFuse Bay (variable size)



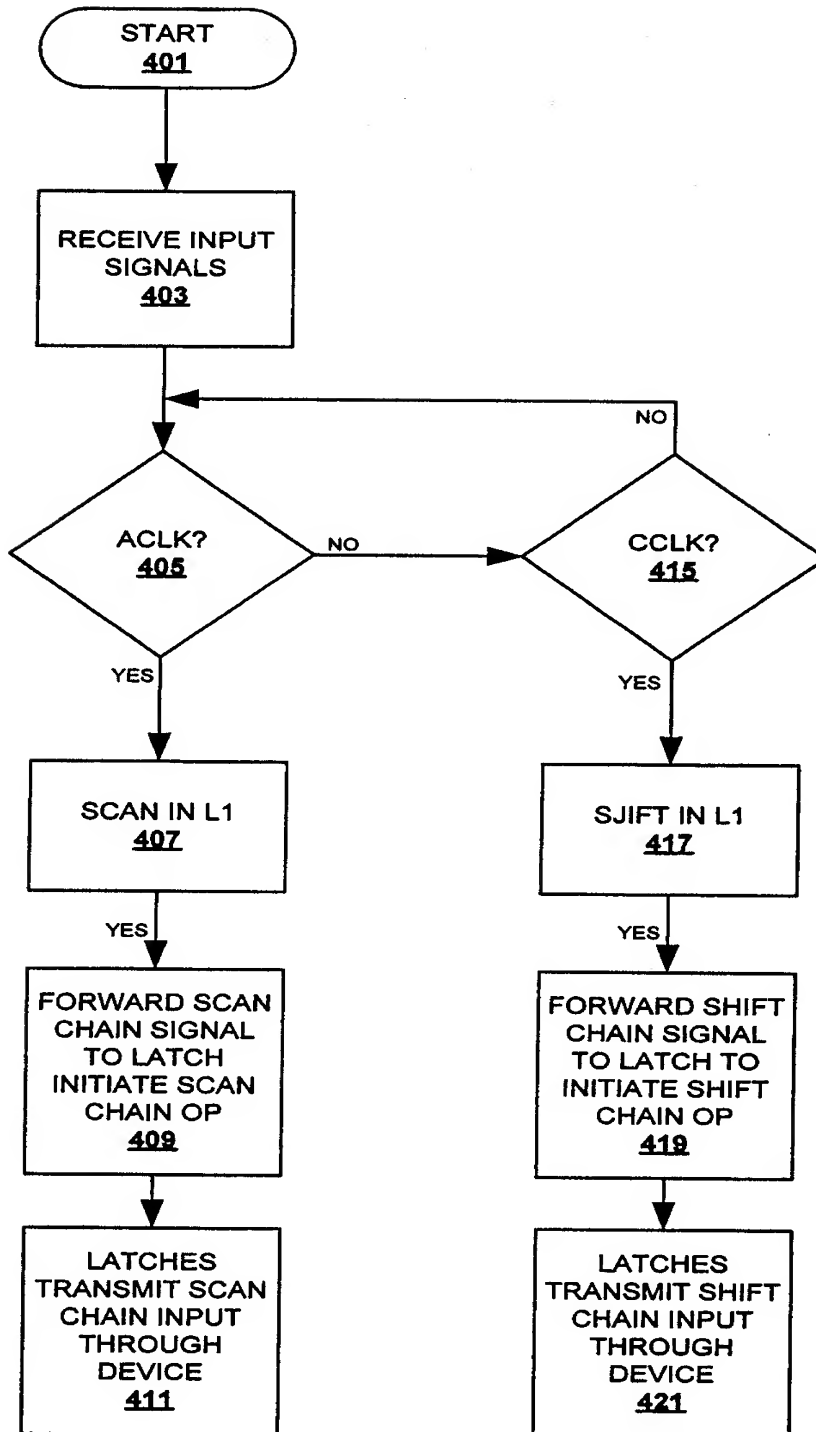


FIGURE 4